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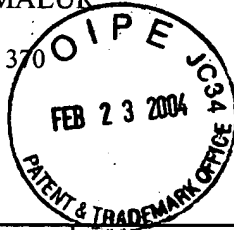
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11/26/2003

MATTINGLY, STANGER & MALUR
ATTORNEYS AT LAW
1800 DIAGONAL ROAD, SUITE 370
ALEXANDRIA, VA 22314



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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/086,718	03/04/2002	Hirokazu Miyagawa	H-1031	2190

TITLE OF INVENTION: MODULATION SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND TESTING METHOD FOR OSCILLATION CIRCUIT

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$300	\$1630	02/26/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
NGUYEN, VINCENT Q	2858	324-076790

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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(B) RESIDENCE: (CITY and STATE OR COUNTRY)

RENASAS TECHNOLOGY CORP.

Tokyo, Japan

Please check the appropriate assignee category or categories (will not be printed on the patent); ☐ individual ☒ corporation or other private group entity ☐ government

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(Authorized Signature) Daniel J. Stanger (Date) 2/23/04

Daniel J. Stanger; Reg. No. 32,846 2/23/04

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